PADS HyperLynx DRC
Option for PADS Standard and PADS Standard Plus

MAJOR BENEFITS:

■ Quickly identify Electromagnetic Interference (EMI) and Signal Integrity (SI) issues with eight built-in checks
■ Efficient design rule checking using an advanced geometric engine
■ Easy to set up and use

OVERVIEW

PADS® HyperLynx® DRC provides a customizable and powerful electrical rule checker for PCB designs. Instead of traditional PCB checks such as trace-to-trace spacing and trace-to-board edge boundary, PADS HyperLynx DRC includes a comprehensive rule set that identifies layout oversights that frequently cause EMI or SI issues.

Perform design rule checks on boards for electro-magnetic interference and signal integrity issues with PADS HyperLynx DRC.
Eight Built-In DRCs

PADS DRC includes standard checks for items related to Electromagnetic Interference (EMI) and Signal Integrity (SI). Many of the checks look for common or hard-to-find items that cannot be simulated easily, such as traces crossing splits and reference plane changes.

By identifying issues with DRC at the layout phase, errors can be caught and fixed prior to board release. Electrical rule violations identified by DRC can also guide you to areas that can be investigated with more in-depth SI/PI/3D tools.

Easy Setup and Navigation

PADS HyperLynx DRC is designed for ease of use. A built-in Setup Wizard will have you running design checks on your board in no time. Items such as electrical model assignment, connector definition, power/ground net definition, specifying discrete components, and electrical net definition are all prompted for in the Setup Wizard.

A simple wizard-based user interface offers a check-list-based selection of DRCs to run along with the ability to filter and view violations by DRC type, electrical net, physical net, or component.
Annotations

From the project explorer view, you can mark up the violations with notes to aid in the design review and resolution process.